

Comparison of return loss calculations with measurements of narrow-band microstrip patch antennas

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Abstract: The return loss of rectangular, single layer, coax fed patch antennas designed to resonate at 1904 MHz was computed using WIPL-D and HFSS and the results compared with experiment. Both codes predicted the bandwidth at -10 dB return loss points with good accuracy but not the resonant frequency and the corresponding return loss. It was also found that the resonant frequency predicted by WIPL-D was substantially closer to the experimental value than that predicted by HFSS.

1. INTRODUCTION

With growing use of microstrip antennas in a variety of applications, there is a great need for modeling and analysis software packages that can accurately and efficiently predict the performance of these antennas. The cost, ease of use, and the time spent in both the design and calculations are additional and important key factors in selecting a software package. A number of microstrip antennas modeling software packages are compared in [1] where it is shown that custom-written codes are more accurate than standard commercial packages. In this paper designs of single-layer coax fed rectangular microstrip patch antennas are examined using WIPL-D and HFSS (High-Frequency Structure Simulator) software packages and the results compared with experiment.

WIPL-D is a frequency domain moment method program that models metallic and/or dielectric/magnetic structures (antennas, scatterers, passive microwave circuits, etc.) [2]. The geometry of the structure is defined in an interactive way using a combination of wires, plates and material objects. HFSS is a frequency domain finite element code. The version used in this paper is Agilent Version 5.6.

Instead of using the performance measures introduced in [1] here comparisons of computed and experimental results employ the return loss as a function of frequency.

The initial patch dimensions and substrate thickness were chosen on the basis of a standard simplified

transmission line model [3], [4] and subsequently adjusted in accordance with repeated applications of WIPL-D. Once the dimensions and feed point position that yield a match at the desired resonant frequency (1904 MHz) were determined the return loss was computed as a function of frequency. Both a single patch and a 3-element linear patch array were modeled. The calculations were then repeated with HFSS. Several patch antennas were fabricated with dimensions held to within 10 microns of the values used in the calculations. Special care was exercised to duplicate the physical parameters of the coaxial feed used in the computer models. Measurements of return loss were carried out using a carefully calibrated HP 8722D network analyzer.

2. PATCH GEOMETRY AND COAXIAL FEED

Fig. 1 shows the geometry of a single-layer patch antenna, where L denotes the length and W the width. The patch is fed by a coaxial line (Fig. 2) with the feed point located at X_r and $Y_r = W/2$. The dimensions of the rectangle defining the extent of the ground plane are denoted by u and v . Figure 2 shows the cross-section of the rectangular patch and substrate in the E-plane. The length of the coaxial line is T and the radii of the inner and outer conductors are r_0 and r_1 , respectively. The dielectric constant of the coaxial line is ϵ_1 and only the inner conductor is extended into the substrate. The coaxial line characteristic impedance was kept at 50 Ohms with r_0 and r_1 as well as the dielectric constant chosen to corresponding to a standard SMA connector.

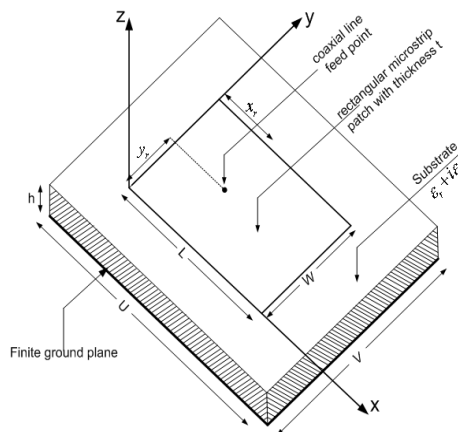


Fig. 1. Rectangular microstrip patch.

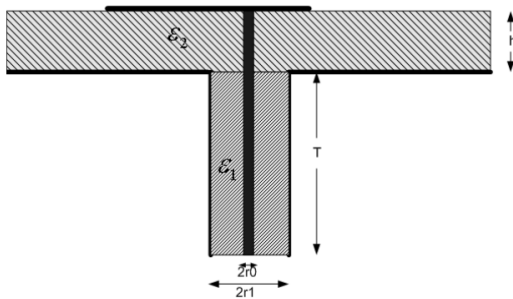


Fig. 2. Rectangular microstrip patch, E-plane cut.

3. COMPARISON OF RETURN LOSS COMPUTED USING WIPL-D AND HFSS

The computations assumed perfectly conducting patches with a thickness of 35 microns. The substrate material was RT/duroid 5880 with manufacturer’s specified dielectric constant $\epsilon_2 = 2.2(1 - i0.0009)$ (where the loss tangent corresponds to vendor’s measured value at 10GHz) and a thickness $h = 1.575mm$. It is worth mentioning that WIPL-D simulations with loss tangents between 0.0009 and 0.0004 (the vendor’s value at 1 MHz) yielded negligible differences both in regard to bandwidth and resonant frequency. Calculations were performed for a single patch radiator and a linear array of three identical patches aligned in the E-plane. Referring to Fig. 2 the pertinent dimensions for the single patch were $L=51.22\text{ mm}$, $W=60mm$, $X_r=0.35L$, $Y_r = W/2$, $u=L+40h$, and $v=W+40h$.

The accuracy levels in WIPL were chosen to be enhanced 2 for both the current expansion and integral accuracy. In addition, double edging (finer resolution) around the patch edges were specified [2]. In HFSS, the *port* boundary condition is used. Furthermore, the *ground plane* boundary condition is used in the model. The final calculations of the field and the S-parameters depend on the precision of the mesh and hence mesh refinement is performed. The frequency of refinement was chosen in such a manner that the return loss at this frequency is expected to be 10 dB or better.



Fig. 3. Top and bottom views of typical test patches.

Two sets of identical patches and arrays were

fabricated. A panel comprised of 35 micron (1 oz) rolled copper on both sides with RT/5880 Duroid 0.062” substrate sandwiched in between was used for this purpose. One side of the panel was etched to produce patch radiators with the desired dimensions while on the other side SMA flanged 50 Ohm connectors were attached with screws to ¼ inch thick copper extenders that were glued to the 35 micron copper ground plane using a highly conducting epoxy. The top and bottom of the resulting configurations are shown in Fig. 3. The return loss was measured using the HP 8722D network analyzer. The three-element array was centered on a ground plane/substrate with dimensions $u=114.2\text{ mm}$ and $v=304.8\text{ mm}$, as shown in Fig. 4.

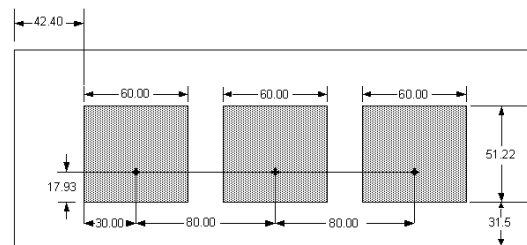


Fig. 4. 3-element patch array.

To test for measurement repeatability and for effects of possible dimensional deviations two sets of identical configurations were fabricated and measured. No differences were discerned in the measured return loss. In Fig. 5 the plots of the return loss for the single element patch antenna computed by WIPL-D and HFSS are compared with the experimental curve. The measured results show that the patch resonates at 1909 MHz. Clearly the resonances predicted by both WIPL-D (1904 MHz) and HFSS (1886 MHz) deviate from the experimental value.

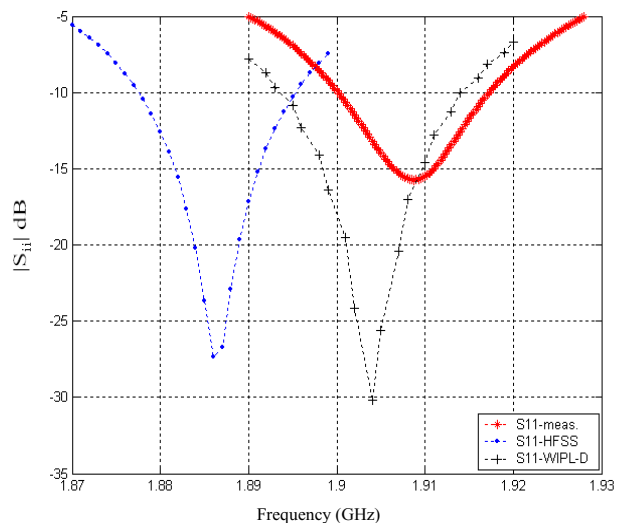


Fig. 5. Return loss predicted by WIPL-D and HFSS with for a single patch compared with experiment.

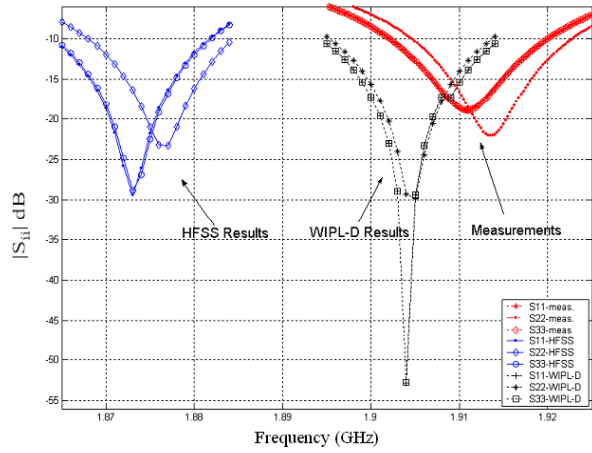


Fig. 6. Return loss predicted by WIPL-D and HFSS with for a 3-element patch compared with experiment.

The extent to which the disagreements of calculations with measurement can be attributed to deviations of the dielectric constant from its nominal value of 2.20 was investigated by carrying out the return loss computations with WIPL-D over the full tolerance range (2.19-2.22) certified by the vendor (Rogers Corp.) The results are plotted in Fig. 7. As shown in the figure, the predicted resonant frequency varied from 1.896 GHz to 1.908 GHz (The return loss curve corresponding to the nominal value of 2.2 is indicated by the thick solid line.).

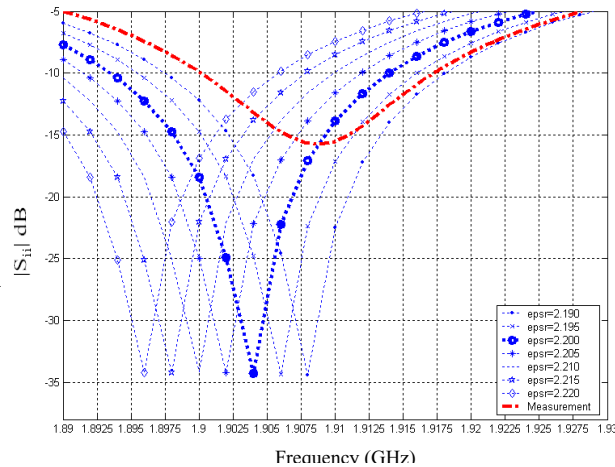


Fig. 7. WIPL-D calculations showing the change in the resonant frequency when the substrate dielectric constant was varied over the tolerance range certified by the vendor.

4. CONCLUDING REMARKS

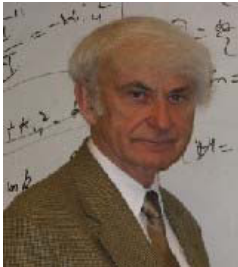
The plots in Figs. 5 and 6 show noticeable differences between WIPL-D and HFSS modeling capabilities, with WIPL-D results closer to experimental data.

5. REFERENCES

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Kunal Parikh was born on June 14, 1980 in Ahmedabad, India. He earned his Bachelors of Engineering degree in Electronics & Communications from Nirma Institute of Technology affiliated with the Gujarat University in 2001. He then joined the Blacksburg campus of Virginia Tech as a Master's student in the Bradley Department of Electrical and Computer Engineering. In the summer of 2002, he moved to the Alexandria Research Institute of the Northern VA campus, where he worked as a Graduate Research Assistant for the rest of his program. This work was carried out as part of his Master's thesis. Kunal is now an employee of LCC International, Inc. and works as an RF Design & Optimization engineer.



Amir I. Zaghloul received the Ph.D. and M.A.Sc degrees from the University of Waterloo, Canada in 1973 and 1970, respectively, and the B.Sc. degree (Honors) from Cairo University, Egypt in 1965, all in electrical engineering. In 2001 he joined Virginia Polytechnic Institute and State University (Virginia Tech) as Professor in the Bradley Department of Electrical and Computer Engineering. Prior to Virginia Tech., he was at COMSAT Laboratories for 24 years performing and directing R&D efforts on satellite communications and antennas, where he received several research and patent awards, including the Exceptional Patent Award. He held positions at the University of Waterloo, Canada (1968-1978), University of Toronto, Canada (1973-74), Aalborg University, Denmark (1976) and Johns Hopkins University, Maryland (1984-2001). He is a Fellow of the IEEE and the recipient of the 1986 Wheeler Prize Award for Best Application Paper in the IEEE Transactions on Antennas and Propagation. He is also an Associate Fellow for The American Institute of Aeronautics and Astronautics (AIAA), a Member of Commissions A & B of the International Union of Radio Science (URSI), and member of the IEEE Committee on Communications and Information Policy (CCIP). Dr. Zaghloul is the general chair of the upcoming "IEEE International Symposium on Antennas and Propagation and USNC/URSI Meeting," which will take place in Washington, D.C. in 2005.