

Simulation of Non Linear Circuits by the Use of a State Variable Approach in the Wavelet Domain

S. Barmada, A. Musolino, and M. Raugi

Department of Electrical Systems and Automation
University of Pisa, Via Diotisalvi 2, 56126 Pisa, Italy
sami.barmada@dsea.unipi.it

Abstract — A method for the simulation of complex circuits with nonlinear elements is proposed. The method is based on wavelet expansion of the state variable description, and leads to a compact representation of the nonlinear problem which is characterized by accuracy and computational efficiency.

Keywords — Circuit Simulation, Nonlinear Loads, Wavelet Expansion.

I. INTRODUCTION

The most common approach to design and optimize microwave devices is to represent them through equivalent circuits, which are in general composed by linear, non linear, lumped, and distributed elements.

The main difficulty of the problem is the mixed nature between linear components (best treated in the frequency domain) and nonlinear components (best treated in the time domain). Some authors proposed the impulse response and convolution (IRC) technique [1] – [3], in which a great computational effort is dedicated to the process of recording and convolving the quantities related to the nonlinear elements. A different approach consists of approximating the frequency response by a Padè approximation, so called asymptotic waveform evaluation method, and has been used for both linear and nonlinear circuits [4] – [6]; the main drawback of the method is the sometimes low approximation (due to the reduction in the number of poles) in case of very complex circuits. In [7] a further approach is presented: the numerical inversion of Laplace transform technique, which is characterized by several advantages with respect to IRC and AWE, but suffers from the series approximations and the nonlinear iterations involved.

Recently wavelet based techniques have been proposed also for the analysis of nonlinear circuits (in transient or steady state mode), showing good potential [8], [9]. A basis of Daubechies wavelets on the interval is here used to expand the unknown quantities and the circuit equations are obtained by the application of the modified nodal analysis. The nonlinearities are treated by the use of the substitution theorem, and the problem is solved by the application of a standard Newton –

Raphson algorithm with an analytical calculation of the Jacobian.

This particular formulation makes the method efficient from a computational point of view, since the matrices involved in the calculation are sparse (i.e. characterized by a small number of non zero elements), hence the number of multiplications required in the solution is low. Furthermore the characteristics of the chosen wavelet basis reduce the size of the matrix.

The method has been tested in several cases, here is reported the calculation of voltages and currents in a complex circuit, and the results are compared with the results coming from a SPICE simulation.

II. MATHEMATICAL FORMULATION

A. Modified Nodal Analysis in the Wavelet Domain

Let us consider a complex circuit, composed by a set of lumped and distributed parameters, connected to independent voltage generators and linear loads. It is possible to divide the circuit into two interconnected parts: a subnetwork φ composed by lumped linear and nonlinear elements, for which we are interested in the calculation of voltages and currents, and a linear subnetwork π , composed by lumped and/or distributed elements, seen as a multiple port circuit. By the used of the MNA it is possible to write the circuit equations in the Laplace domain in the following form [10]

$$\mathbf{W}_\varphi s \mathbf{x}_\varphi(s) + \mathbf{G}_\varphi \mathbf{x}_\varphi(s) + \mathbf{L}_\pi(s) \mathbf{i}_\pi(s) = \mathbf{b}(s) \quad (1)$$

where \mathbf{x}_φ is the vector of unknowns (nodal voltages, whose dimension is the total number of MNA variables); \mathbf{W}_φ and \mathbf{G}_φ are constant matrices describing the lumped elements of the network φ and \mathbf{b} is a constant vectors whose entries are the independent voltage and current sources (together with the initial condition sources, if present). \mathbf{L}_π is a matrix whose entries are zeroes or ones, mapping the vector $\mathbf{i}_\pi(s)$ of currents entering the linear subnetwork π into the node space of

the network φ . The linear multiterminal subnetwork π can be described by the standard approach as

$$\mathbf{Y}_\pi(s)\mathbf{V}_\pi(s)=\mathbf{i}_\pi(s) \quad (2)$$

where $\mathbf{Y}_\pi(s)$ is the y parameter matrix and $\mathbf{V}_\pi(s)$ is the vector of terminal voltage nodes connecting the subnetwork to the network φ . By substituting (2) in (1) it is possible to write

$$\tilde{\mathbf{W}}_\varphi \mathbf{s}\mathbf{x}_\varphi(s) + \tilde{\mathbf{G}}_\varphi \mathbf{x}_\varphi(s) = \mathbf{b}(s) \quad (3)$$

where $\tilde{\mathbf{G}}_\varphi$ and \mathbf{W}_φ take also into account the contribution of the matrix $\mathbf{Y}_\pi(s)$.

As an example, we apply the MNA to the simple network represented in Fig. 1.

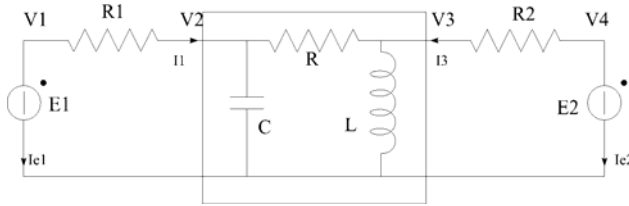


Fig. 1. Simple network for MNA analysis.

In this case equations (1) and (2) respectively become

$$\begin{bmatrix} G_1 & -G_1 & 0 & 0 & 1 & 0 \\ -G_1 & G_1 & 0 & 0 & 0 & 0 \\ 0 & 0 & G_2 & -G_2 & 0 & 0 \\ 0 & 0 & -G_2 & G_2 & 0 & 1 \\ 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \\ v_3 \\ v_4 \\ i_{e1} \\ i_{e2} \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ 1 & 0 \\ 0 & 1 \\ 0 & 0 \\ 0 & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_1 \\ i_3 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ E_1 \\ E_2 \end{bmatrix} \quad (4)$$

$$\begin{bmatrix} i_1 \\ i_3 \end{bmatrix} = \begin{bmatrix} sC & -(sC+G) \\ -(1/sL+G) & 1/sL \end{bmatrix} \begin{bmatrix} v_2 \\ v_3 \end{bmatrix}. \quad (5)$$

We now consider a wavelet basis $\mathbf{b}(t)=[b_1(t), \dots, b_n(t)]$ on the interval $[0, T_m]$; by performing the Wavelet Expansion (WE) of a generic function $f(t)$, we obtain a vector of coefficients of dimension n ; the notation used is the following: $f(t) = \mathbf{b}\mathbf{f} = \sum_j b_j(t)f_j$, where $\mathbf{f}=[f_1, \dots, f_n]^T$ is the vector of the wavelet coefficients. By using the

differential operator \mathbf{D} and the integral operator \mathbf{I} in the wavelet domain (for wavelets on the interval, introduced in [11]) the differentiation (or integration) of a function is simply performed by a matrix – vector product, i.e.,

$$h(t) = \frac{df(t)}{dt} \Rightarrow \mathbf{h} = \mathbf{D}\mathbf{f}. \text{ Formally this means that it is}$$

possible to obtain the equation in the wavelet domain by simply using the Laplace domain equations and substitute the variables with the vectors of coefficients and the operator s with the differential matrix \mathbf{D} . According to this, equation (3) can be expressed as

$$(\tilde{\mathbf{W}}_\varphi + \tilde{\mathbf{G}}_\varphi)\mathbf{x}_\varphi = \mathbf{T}\mathbf{x}_\varphi = \mathbf{b} \quad (6)$$

i.e., an algebraic systems of the form $\mathbf{T}\mathbf{x}_\varphi = \mathbf{b}$ in which the matrix \mathbf{T} is straightforwardly calculated, the vector \mathbf{b} contains the WE of the independent generators and \mathbf{x}_φ is the vector of unknowns (the wavelet coefficients of the expansion of the unknown voltages).

For the example of Fig. 1 we obtain for the matrix

$$\tilde{\mathbf{W}}_\varphi + \tilde{\mathbf{G}}_\varphi = \mathbf{T} \begin{bmatrix} G_1\mathbf{U} & -G_1\mathbf{U} & 0 & 0 & \mathbf{U} & 0 \\ -G_1\mathbf{U} & G_1\mathbf{U} + \mathbf{C}\mathbf{D} & -\mathbf{C}\mathbf{D} - G_1\mathbf{U} & 0 & 0 & 0 \\ 0 & -\mathbf{I}/L - G_1\mathbf{U} & \mathbf{I}/L + G_2\mathbf{U} & -G_2\mathbf{U} & 0 & 0 \\ 0 & 0 & -G_2\mathbf{U} & G_2\mathbf{U} & 0 & \mathbf{U} \\ \mathbf{U} & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & \mathbf{U} & 0 & 0 \end{bmatrix} \quad (7)$$

where \mathbf{U} is the identity matrix of the proper dimension. As it can be easily seen the matrix \mathbf{T} is sparse and the system can be easily and conveniently solved by an iterative technique, requiring a low CPU time consumption.

B. Treatment of the Nonlinearities

The presence of nonlinearities (connected at the output ports) leads to an additional term in equation (1)

$$\mathbf{W}_\varphi \mathbf{s}\mathbf{x}_\varphi(s) + \mathbf{G}_\varphi \mathbf{x}_\varphi(s) + \mathbf{L}_\pi(s)\mathbf{i}_\pi(s) + \mathbf{L}[\mathbf{F}(\mathbf{x}_\varphi(t))] = \mathbf{b}(s) \quad (8)$$

where $\mathbf{F}(\mathbf{x}_\varphi(t))$ represents the above mentioned nonlinearities.

For the sake of simplicity we here refer to a simple two port network, represented in Fig. 2a, where one port is connected to an independent generator, while the other port is connected to a nonlinear load, whose constitutive

relation is $i = f(v)$. The extension to a more complex circuit (i.e. characterized by several ports, connected to generators, linear and nonlinear loads) is straightforward.



Fig. 2a. Circuit with a nonlinear load.



Fig. 2b. Application of the substitution's theorem.

It is possible to substitute the nonlinear load with a voltage generator imposing the unknown voltage $v_{NL}(t)$, as shown in Fig. 2b. Let us suppose that we are interested in calculating the current i_{NL} flowing through the nonlinear load. By applying the superposition effect (once the substitution has been performed the circuit is linear) we can write

$$i_{NL} = i_{NL}^a + i_{NL}^b \quad (9)$$

where the first term i_{NL}^a is related only to the presence of the independent generator E and can be calculated by simply solving a linear problem (of the kind reported in equation (6)), while i_{NL}^b is the current related to the effect of the unknown voltage generator and needs of course to be calculated. Under these assumptions we can write in the wavelet domain

$$\mathbf{i}_{NL} = \mathbf{i}_{NL}^a + \mathbf{i}_{NL}^b = \mathbf{i}_{NL}^a + \mathbf{T}\mathbf{v}_{NL} \quad (10)$$

where the vectors represent the WE of the currents, the matrix \mathbf{T} is the matrix solving the linear problem (see equation (6)), and \mathbf{v}_{NL} is the wavelet expansion of the unknown voltage source. At this stage equation (10) is characterized by two vectors of unknowns: \mathbf{i}_{NL} and \mathbf{v}_{NL} . The additional equation needed to solve the problem is the constitutive equation of the nonlinear load and it is enforced as follows.

By inverse transforming (10) we obtain the following time domain expression of the unknown current

$$i_{NL}(t) = i_{NL}^a(t) + \sum_k \sum_j T_{kj} v_{NL,j} b_k(t) \quad (11)$$

where the terms $b_k(t)$ are the function of the wavelet basis, $v_{NL,j}$ are the entries of the vector \mathbf{v}_{NL} , while the terms T_{kj} are the entries of the square matrix \mathbf{T} . Equation (11) must satisfy the constitutive equation of the nonlinear load $i = f(v)$; in order to enforce it we impose the collocation at the discrete times t_n , n equally spaced points in the interval $[0, T_m]$ (the ones which are characteristic of the definition of the wavelet functions [12]) obtaining a set of nonlinear equations in the unknown coefficients $v_{NL,j}$.

The constitutive equation of the nonlinear load $i - f(v) = 0$ can be written as

$$i_{NL}^a(t) + \sum_k \sum_j T_{kj} v_{NL,j} b_k(t) - f\left(\sum_j v_{NL,j} b_j(t)\right) = 0 \quad (12)$$

i.e., $F(\mathbf{v}_{NL}) = 0$ in which we underline that the unknowns are the coefficients $v_{NL,j}$. The analytical evaluation of the Jacobian is straightforwardly written as follows

$$\frac{\partial F}{\partial v_{NL}} = \sum_k T_{kj} b_k(t_n) - \frac{\partial f}{\partial v} b_j(t_n). \quad (13)$$

The solution of the system is performed by adopting a Newton – Raphson algorithm, with an analytical evaluation of the Jacobian. The convenience of the proposed method, in terms of low CPU time consumption in the presence of nonlinear loads, stands in the availability of the analytical form of the Jacobian; as a matter of fact its knowledge allows us to use a sparsification procedure which results in a reduction of the CPU time employed for the solution, as explained in the next section.

C. Computational Cost of the Proposed Method

The main advantage in using a wavelet basis stands in the fact that it is possible to represent very complex waveforms (typical of fast electrical transients, like the ones in microwave circuits) by a small number of wavelet functions (hence by a small number of coefficients). This leads to a reduced dimension of the matrices involved in the simulations, with respect to other standard techniques.

Furthermore it is well known that wavelets tend to concentrate in time zones where approximation needs to be high and have a reduced weight (low wavelet coefficients) in the other zones, a property known in the literature as self adaptive zooming. According to this observation, the use of a thresholding procedure is a common practice to reduce the computational cost [12], [13]. The coefficients of the wavelet matrix that are smaller than a fraction (typically a few thousandths) of the maximum are forced to zero.

This procedure does not significantly affect the accuracy of the computations (when reasonable values of the threshold are used) and produces sparse matrices that can be efficiently stored and computed. In particular in [12] it is shown that for diagonal dominant matrices it is possible to obtain a percentage of non-zero elements of the order between 5% and 15% with solutions that are affected by extremely low error. The sparsification procedure is convenient because the number of multiplications required for the simulation is considerably lower with respect to the full matrix, leading to a significant CPU time reduction and a lower storage memory.

It is noteworthy that a lower approximation in the evaluation of the Jacobian (which is exactly calculated by the analytical computation as in (13) and post processed by the sparsification procedure) does not affect the correctness of the solution, and may have effects only on the number of iterations necessary to reach convergence.

In the evaluation of the overall computation time reduction we have to consider the weight of the wavelet transform of the input quantities and the inverse wavelet transform of the results. These operations are efficiently performed via matrix vector products characterized by sparse matrices [14]. As a consequence the reduction of the time needed to perform the matrix vector product obtained with the threshold procedure has a strong impact on the overall computation cost, as it involves the most time consuming activity.

III. NUMERICAL APPLICATION

As an example of application of the proposed technique we considered the circuit schematically shown in Fig. 3. It is composed by four Multiconductor Transmission Lines (three conductors and ground each) represented by blocks B, C, E, and F and by the blocks A and D constituted by three longitudinally disposed resistors of 0.1Ω each. The per unit length parameters of the lines are the same for each line and are here reported

$$R = \begin{bmatrix} 351.63 & 0 & 0 \\ 0 & 366.11 & 0 \\ 0 & 0 & 351.63 \end{bmatrix} \Omega/\text{m}$$

$$L = \begin{bmatrix} 4.3698 \cdot 10^{-7} & 7.675 \cdot 10^{-8} & 1.57 \cdot 10^{-8} \\ 7.675 \cdot 10^{-8} & 4.35 \cdot 10^{-7} & 7.675 \cdot 10^{-8} \\ 1.57 \cdot 10^{-8} & 7.675 \cdot 10^{-8} & 4.3698 \cdot 10^{-7} \end{bmatrix} \text{H/m}$$

$$G = \begin{bmatrix} 7.5 \cdot 10^{-4} & 0 & 0 \\ 0 & 7.5 \cdot 10^{-4} & 0 \\ 0 & 0 & 7.5 \cdot 10^{-4} \end{bmatrix} \Omega^{-1}/\text{m}$$

$$C = \begin{bmatrix} 1.082 \cdot 10^{-10} & -0.197 \cdot 10^{-10} & -0.006 \cdot 10^{-10} \\ -0.197 \cdot 10^{-10} & 1.124 \cdot 10^{-10} & -0.197 \cdot 10^{-10} \\ -0.006 \cdot 10^{-10} & -0.197 \cdot 10^{-10} & 1.082 \cdot 10^{-10} \end{bmatrix} \text{F/m}$$

The lines in B and C are respectively long 10 and 5 cm, while those in E and F have the same length of 15 cm.

A voltage generator of waveform

$$e_1(t) = 10 \cdot e^{-\left(\frac{t-2T}{T}\right)^2} \cdot \sin[2\pi f(t-2T)] V$$

is applied to terminal #1, with $T = 0.11 \text{ ns}$, $f = 2.8 \text{ GHz}$ and characterized by an internal resistive impedance $R_1 = 10 \Omega$.

The amplitude of the waveform $e_1(t)$ shown in Fig. 4, has been chosen in order to strongly evidence the nonlinear effects.

Terminals 2, 3, 5, 6, 8, 9, 11, and 12 are connected to 50Ω termination resistances.

Terminal # 4 is terminated on the series connection of a 15Ω resistance and of a nonlinearity described by the following characteristic

$$v_4(t) = 10^5 \cdot i_4^3(t).$$

Terminals 7 and 10 are terminated on series connection between a 20Ω resistance and a diode of characteristic

$$i_{4,7}(t) = I_0 \cdot \left(e^{v_{4,7}(t)/v_r} - 1 \right)$$

where $I_0 = 1 \text{ pA}$ and $v_r = 0.025865 \text{ V}$.

The system has been simulated by using a basis of 128 Daubechies wavelet with 6 vanishing moments.

The same system has also been simulated with SPICE and the results have been compared with those obtained by the approach here presented.

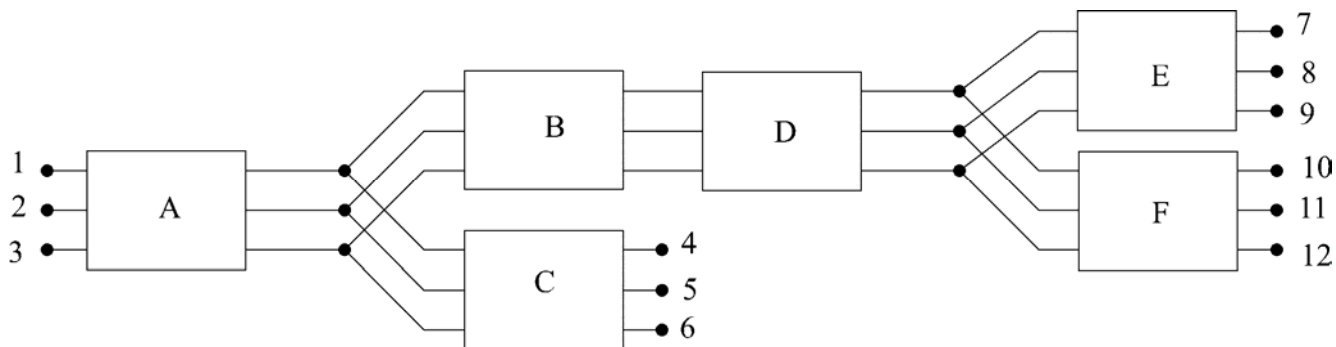


Fig.3. Analysed circuit.

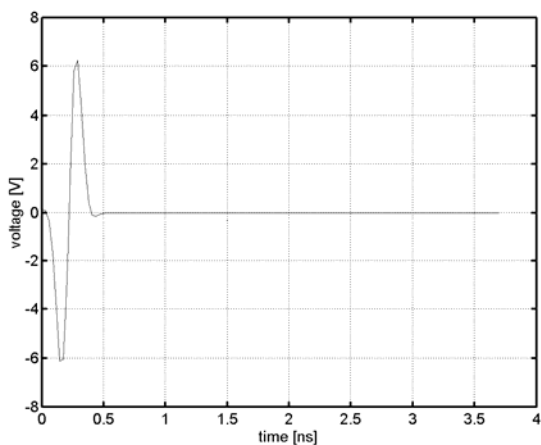


Fig. 4. Input voltage at terminal #1.

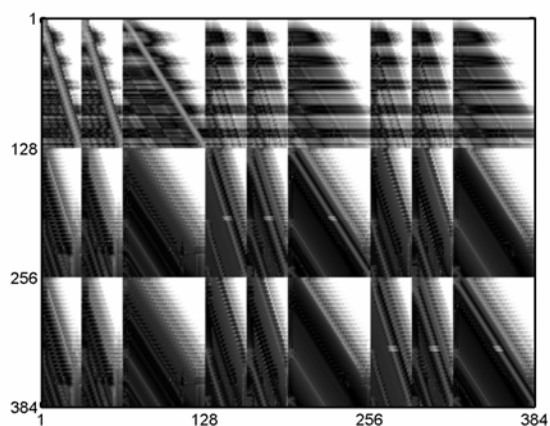


Fig. 6. Typical sparsity pattern of the Jacobian matrix in the version that uses original full matrices.

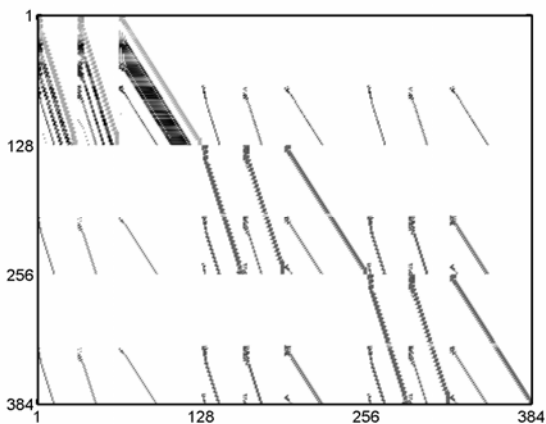


Fig. 5. Typical sparsity pattern of the Jacobian matrix in the version that uses sparse matrices.

Two versions of the proposed method have been implemented: the first one which performs the sparsification in order to take advantage of the available numerical routines for treatment of sparse matrices while the other uses the original full matrices.

The comparison has been performed by considering the Jacobian matrices related to comparable deviation of $F(v_{NL})$ from zero in the cases of solution of the sparsified and original system.

Figures 5 and 6, respectively, show the Jacobian used in Newton-Raphson scheme with and without sparsification.

Figures 7 and 8, respectively, show the voltage and the current on the cubic nonlinearity.

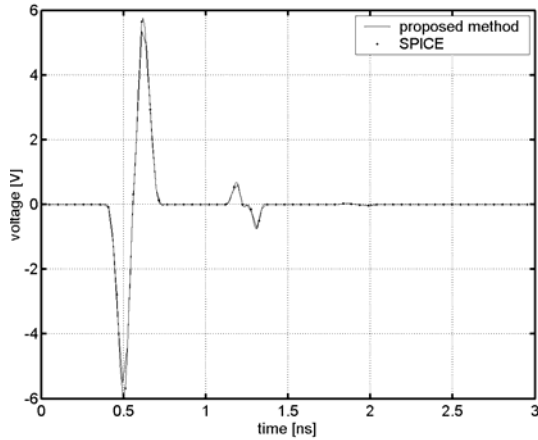


Fig. 7. Comparison between the voltages on the cubic nonlinearity obtained by the proposed approach and by SPICE.

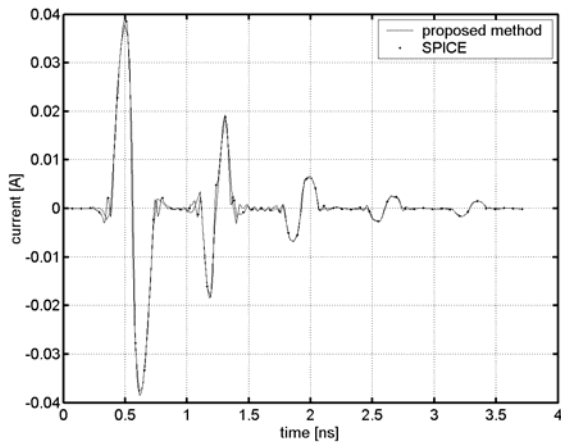


Fig. 8. Comparison between the currents on the cubic nonlinearity obtained by the proposed approach and by SPICE.

The waveforms obtained by the two approaches are practically indistinguishable. In order to appreciate the good agreement between the results a zoom on the voltages is shown in Fig. 9.

Figures 10 and 11 show the voltage and the current on the diodes. As expected the current on the diode is unidirectional and the voltage on the diode does not exceed the value of 0.65 V that approximately represents the direct bias voltage of a silicon diode.

The first procedure has required 23 iterations while the second one 35. The solution of the linear system obtained after the thresholding procedure was about five times faster than that in the other one. The total CPU time (2.8 sec) required to solve the nonlinear problem by using sparse matrices using the standard Newton-Raphson was three times shorter than the modified Newton-Raphson

with full matrices (9.3 sec). The used threshold was 5% of the maximum element of each column.

As for the SPICE simulation the CPU time was about 4 seconds.

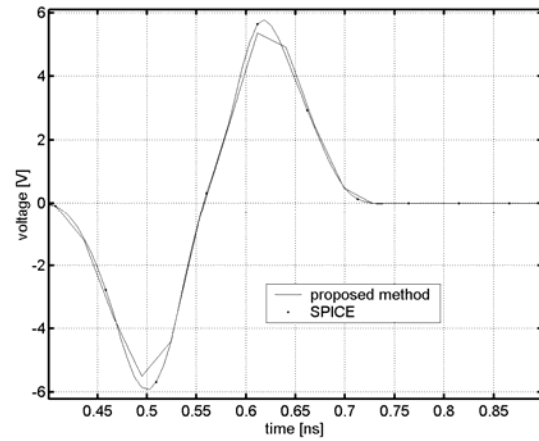


Fig. 9. Enlarged portion of Fig. 7.

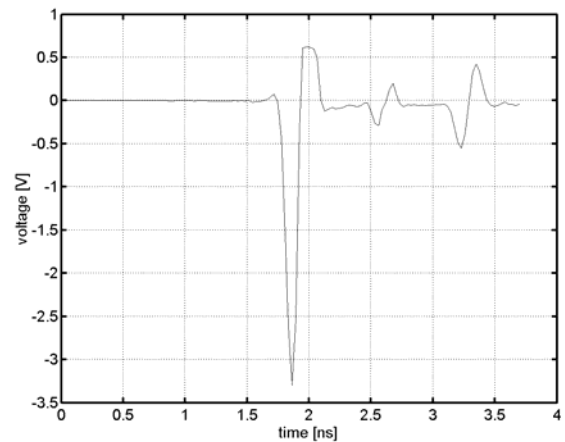


Fig. 10. Voltage on the diodes on terminals #7 and #10.

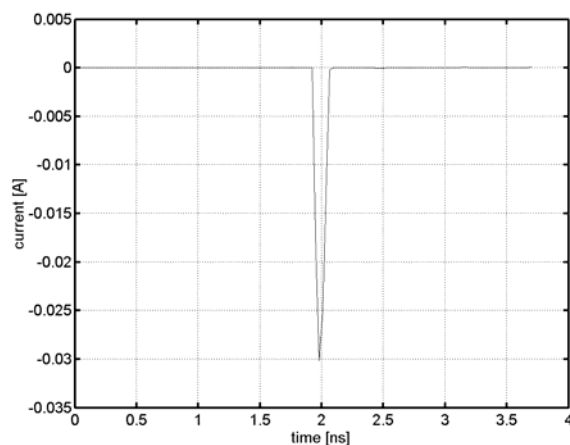


Fig. 11. Current across the diodes on terminals #7 and #10.

IV. CONCLUSIONS

A method for the simulation of complex circuits, in presence of nonlinear elements is here proposed. The method is based on wavelet expansion and a special treatment of the nonlinearity. The method allows a fast computation, together with the required accuracy and low memory consumption.

ACKNOWLEDGMENTS

This work is supported in part by the Italian Ministry of University (MIUR) under a Program for the Development of Research of National Interest (PRIN grant # 2004093025).

REFERENCES

- [1] A. R. Djordjevic and T. K. Sarkar, "Analysis of Time Response of Lossy Multiconductor Transmission Line Networks," *IEEE Trans. Microwave Theory Tech.*, vol 43, pp. 314 – 323, Oct. 1987.
- [2] J. E. Schutt-Aine and R. Mittra, "Nonlinear Transient Analysis of Coupled Transmission Lines," *IEEE Trans. Circuits Syst.*, vol 36, pp. 959 – 967, July 1989.
- [3] P. Stenius, P. Heikkila, and M. Valtonen, "Transient Analysis of Circuits Including Frequency Dependent Components Using Transgyrator and Convolution," *Proc. 11th European Conf. Circuit Theory Design pt. III*, pp. 1299 – 1304, 1993.
- [4] R. Achar, M. S. Nakhla, and Q. J. Zhang, "Full Wave Analysis of High Speed Interconnects Using Complex Frequency Hopping," *IEEE Trans. Computer Aided Design*, vol. 17, pp. 997 – 1010, Oct. 1998.
- [5] E. Chiprout and M. Nakhla, "Fast Nonlinear Waveform Estimation for Large Distributed Networks," *IEEE MTT-S Int. Microwave Symp. Dig.*, vol. 3, pp. 1341 – 1344, June 1992.
- [6] R. J. Trihy and R. A. Rohrer "AWE Macromodels for Nonlinear Circuits," *Proc. 36th Midwest Circuits Syst. Symp*, vol 1., pp. 633 – 636, Aug. 1993.
- [7] R. Griffith and M. S. Nakhla "Mixed Frequency/Time Domain Analysis of Nonlinear Circuits," *IEEE Trans. Computer Aided Design*, vol 11, pp. 1032 – 1043, Aug. 1992.
- [8] N. Soveiko and M. S. Nakhla, "Steady State Analysis of Multitone Nonlinear Circuits in Wavelet Domain," *IEEE Trans. Microwave Theory Tech.*, vol. 52, no. 3, pp. 75 – 797, March 2004.
- [9] D. Zhou, W. Cai, and X. Zeng, "An Adaptive Wavelet Method for Nonlinear Circuit Simulation,"

IEEE Trans. Circuit Systems I, vol. 46, no. 8, pp. 931 – 938, Aug. 1999.

- [10] R. Achar and M. S. Nakhla, "Simulation of High Speed Interconnects," *Proc. of The IEEE*, vol. 89, no. 5, pp. 693 728, May 2001.
- [11] S. Barmada and M. Raugi, "A General Tool for Circuit Analysis Based on Wavelet Transform," *Int. J. of Circuit Theory and Applications*, vol. 28, no. 5, pp. 461 – 480, 2000.
- [12] T. K. Sarkar and M. Salazar – Palma, M. C. Wicks, *Wavelet Applications in Engineering Electromagnetics*. Artech House, 2002.
- [13] R. L. Wagner and W. C. Chew, "A Study of Wavelets for the Solution of Electromagnetic Integral Equations," *IEEE Trans. On Antenna and Prop.*, vol. 43, no. 8 pp. 802-810, Aug. 1995.
- [14] T. Palenius and J. Roos, "Comparison of Reduced-Order Interconnect Macromodels for Time Domain Simulation," *IEEE Trans. Microwave Theory Tech.*, vol. 52, no. 9, pp. 2240-2250, Sept. 2004.
- [15] T. K. Sarkar, C. Su, R. Adve, M. Salazar Palma, L. Garcia Castillo, and R. R. Boix, "A Tutorial on Wavelets from an Electrical Engineering Perspective, Part 1: Discrete Wavelet Techniques," *IEEE Antenna and Propagation Magazine*, vol. 40, no. 5, pp. 49-70, Oct. 1998.



Sami Barmada was born in Livorno, Italy, on November 18, 1970. He received the Master and Ph.D. degrees in electrical engineering from the University of Pisa, Pisa, Italy, in 1995 and 2001, respectively.

From 1995 to 1997, he was with ABB Teknologi, Oslo, Norway, where he was involved with distribution network analysis and optimization. He is currently an Associate Professor with the Department of Electrical System and Automation, University of Pisa, where he is involved with numerical computation of electromagnetic fields, particularly on the modeling of multiconductor transmission lines and to the application of wavelet expansion to computational electromagnetics.

Dr. Barmada was the technical chairman of the Progress In Electromagnetic Research Symposium (PIERS), Pisa, Italy, 2004 and the General Chair of the ACES 2007 Conference, Verona, Italy.

He was the recipient of the 2003 J F Alcock Memorial Prize, presented by The Institution of Mechanical Engineering, Railway Division, for the best paper in technical innovation.



Antonino Musolino was born in Reggio Calabria on January the 7th 1964. He received the Master degree in Electronic Engineering and the Ph. D degree in Electrical Engineering from the University of Pisa in 1990 and 1995 respectively.

He is currently an Associate Professor of electrical engineering with the Department of Electrical Systems and Automation, University of Pisa. His main research activities are the numerical methods for the analysis of electromagnetic fields in linear and nonlinear media, the design of special electrical machines and the application of the WE to computational electromagnetics.



Marco Raugi received the Electronic Engineering degree and Ph.D. degree in electrical engineering from the University of Pisa, Pisa, Italy, in 1985, and 1990, respectively.

He is currently a Full Professor of electrical engineering with the Department of Electrical Systems and Automation, University of Pisa. His research concerns numerical methods for the analysis of electromagnetic fields in linear and nonlinear media. His main applications have been devoted to nondestructive testing, electromagnetic compatibility in TLLs, and electromagnetic launchers. He has authored or coauthored approximately 100 papers in international journals and conferences. He has served as chairman for various Editorial Boards.

Dr. Raugi was the general chairman of the Progress In Electromagnetic Research Symposium (PIERS), Pisa, Italy, 2004. He has served as chairman and session organizer of international conferences.

He was the recipient of the 2002 IEEE Industry Application Society Melcher Prize Paper Award.